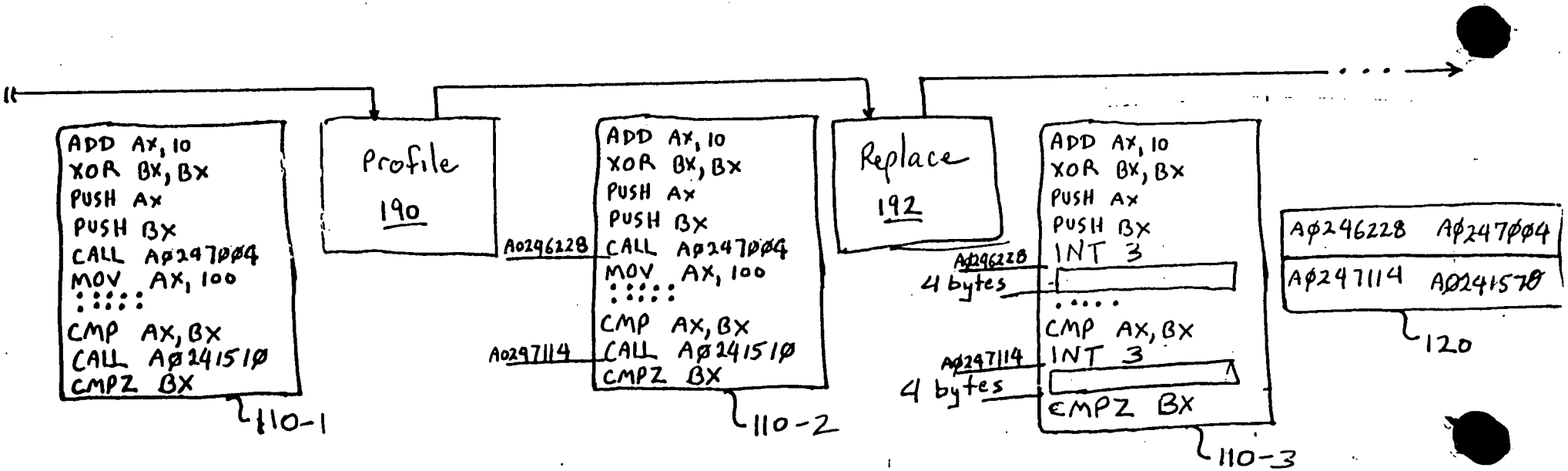

ADD AX, 10
XOR BX, BX
PUSH AX
PUSH BX
CALL Aφ247φφ4
MOV AX, 100
CMP AX, BX
CALL Aφ241510
CMPZ BX
110-1
Profile
190
A0296228
A0297114
ADD AX, 10
XOR BX, BX
PUSH AX
PUSH BX
CALL Aφ247φφ4
MOV AX, 100
CMP AX, BX
CALL Aφ241510
CMPZ BX
110-2
Replace
192
Aφ296228
4 bytes
Aφ297114
4 bytes
ADD AX, 10
XOR BX, BX
PUSH AX
PUSH BX
INT 3
CMP AX, BX
INT 3
EMPZ BX
110-3
Aφ296228 Aφ247φφ4
Aφ297114 Aφ241578
120

FIG. 1A

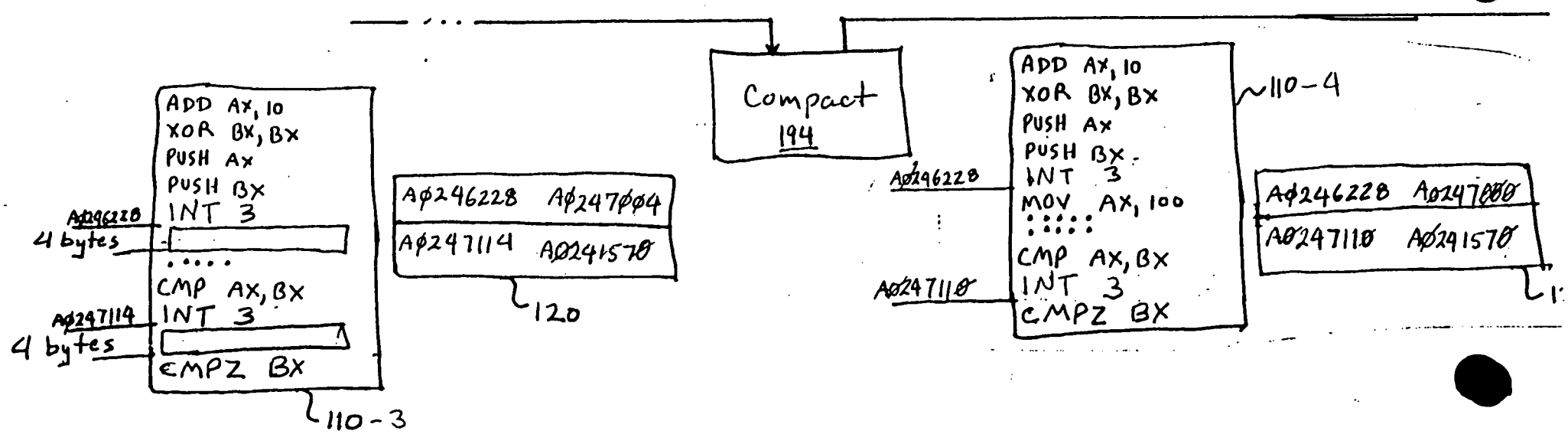

ADD AX, 10
XOR BX, BX
PUSH Ax
PUSH BX
INT 3
A∅246228
4 bytes
CMP AX, BX
INT 3
A∅247114
4 bytes
CMPZ BX
110-3
A∅246228 A∅247∅04
A∅247114 A∅241570
120
Compact
194
ADD AX, 10
XOR BX, BX
PUSH Ax
PUSH BX
INT 3
MOV AX, 100
CMP AX, BX
INT 3
CMPZ BX
110-4
A∅246228
A∅247110
A∅246228 A∅247000
A∅247110 A∅241570

FIG. 1B

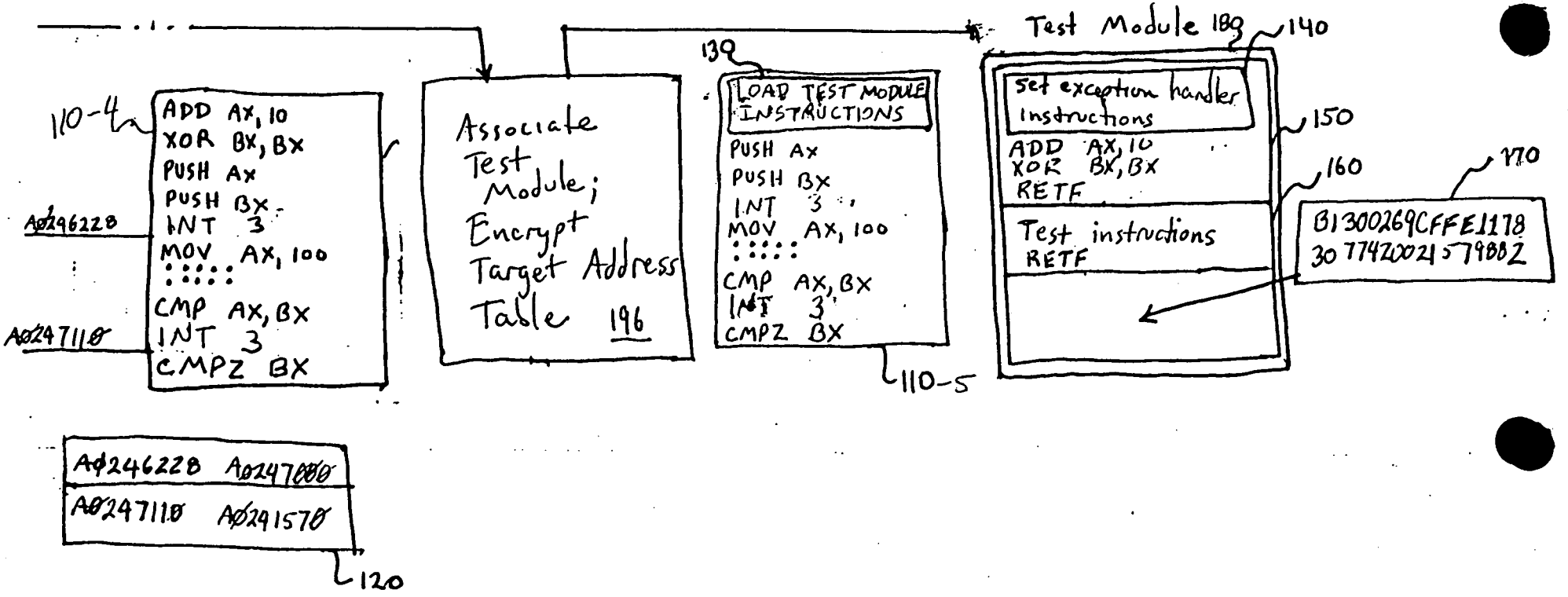

110-4
ADD AX, 10
XOR BX, BX
PUSH AX
PUSH BX
INT 3
MOV AX, 100
CMP AX, BX
INT 3
CMPZ BX
A0246228
A0247118
Associate Test Module; Encrypt Target Address Table 196
139
LOAD TEST MODULE INSTRUCTIONS
PUSH AX
PUSH BX
INT 3
MOV AX, 100
CMP AX, BX
INT 3
CMPZ BX
110-5
Test Module 180
140
Set exception handler instructions
150
ADD AX, 10
XOR BX, BX
RETF
160
Test instructions
RETF
170
B1300269CFFE1178
30774200215798BZ
A0246228 A0247000
A0247118 A0291570
120

FIG. 1C

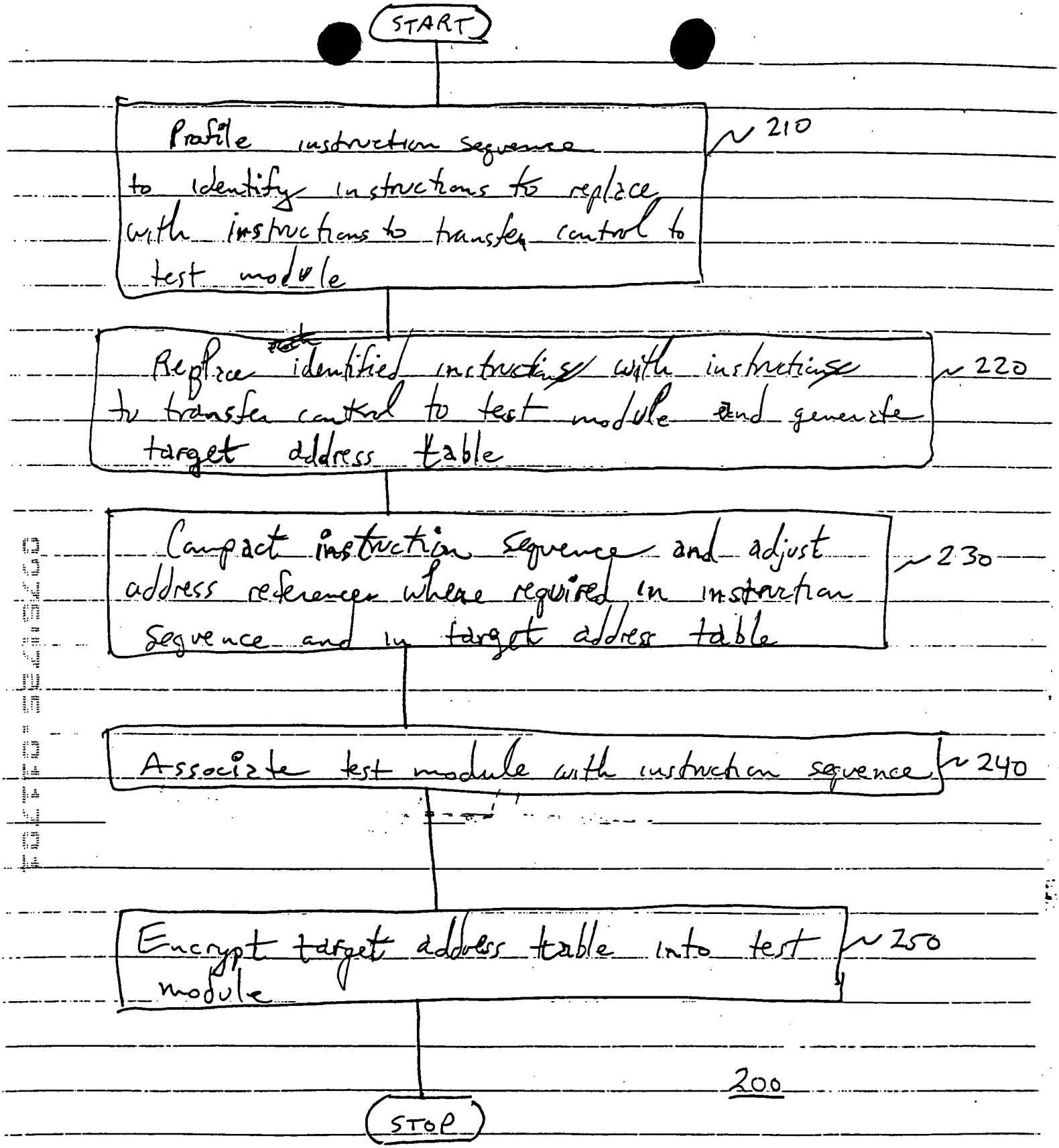

START
Profile instruction sequence to identify instructions to replace with instructions to transfer control to test module
210
Replace identified instructions with instructions to transfer control to test module and generate target address table
220
Compact instruction sequence and adjust address references where required in instruction sequence and in target address table
230
Associate test module with instruction sequence
240
Encrypt target address table into test module
250
200
STOP

FIG. 2

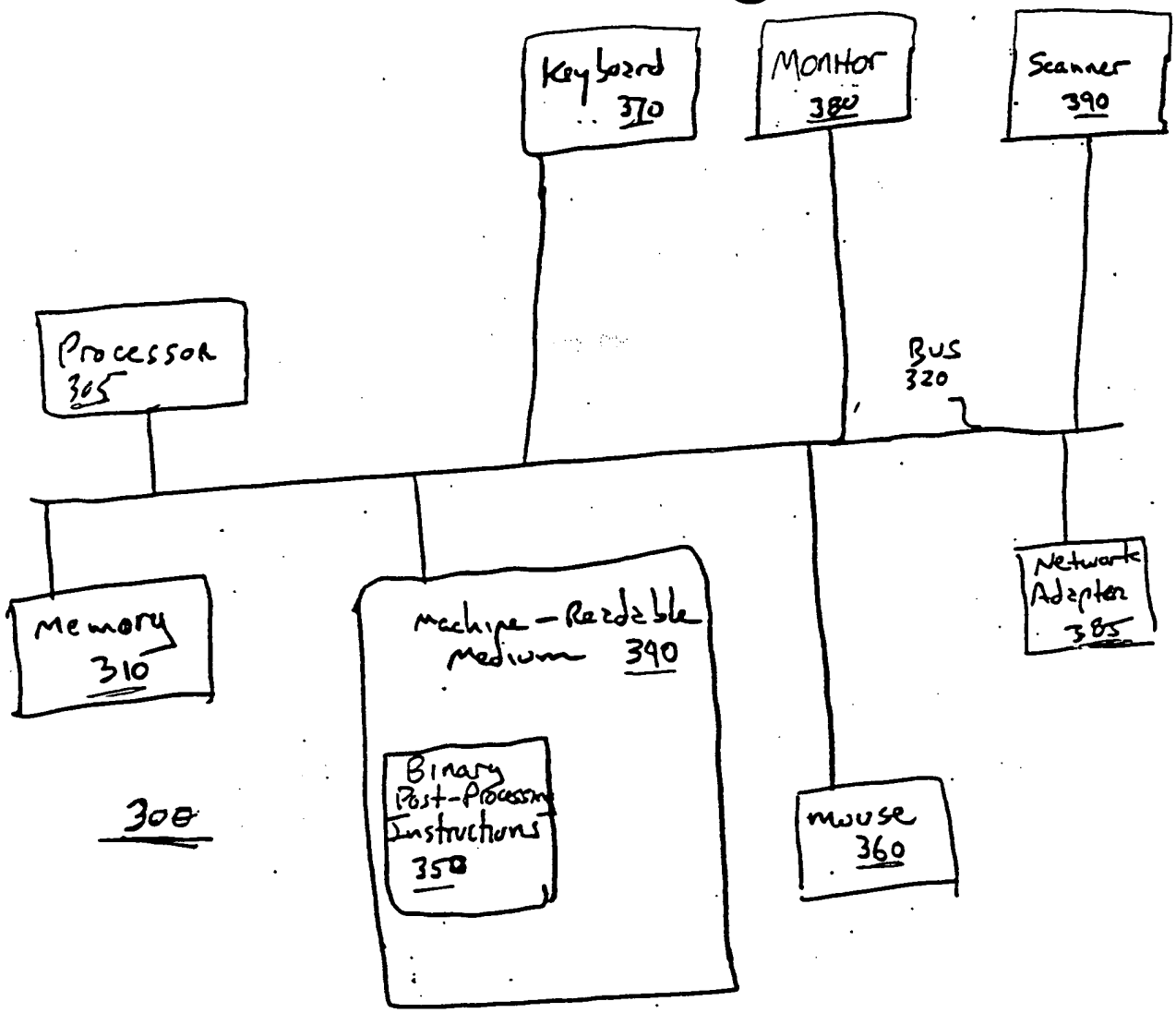

Keyboard
370
Monitor
380
Scanner
390
Processor
305
Bus
320
Memory
310
machine-Readable
Medium 390
Binary
Post-Processor
Instructions
350
Network
Adapter
385
mouse
360
300

FIG. 3

FIG. 4

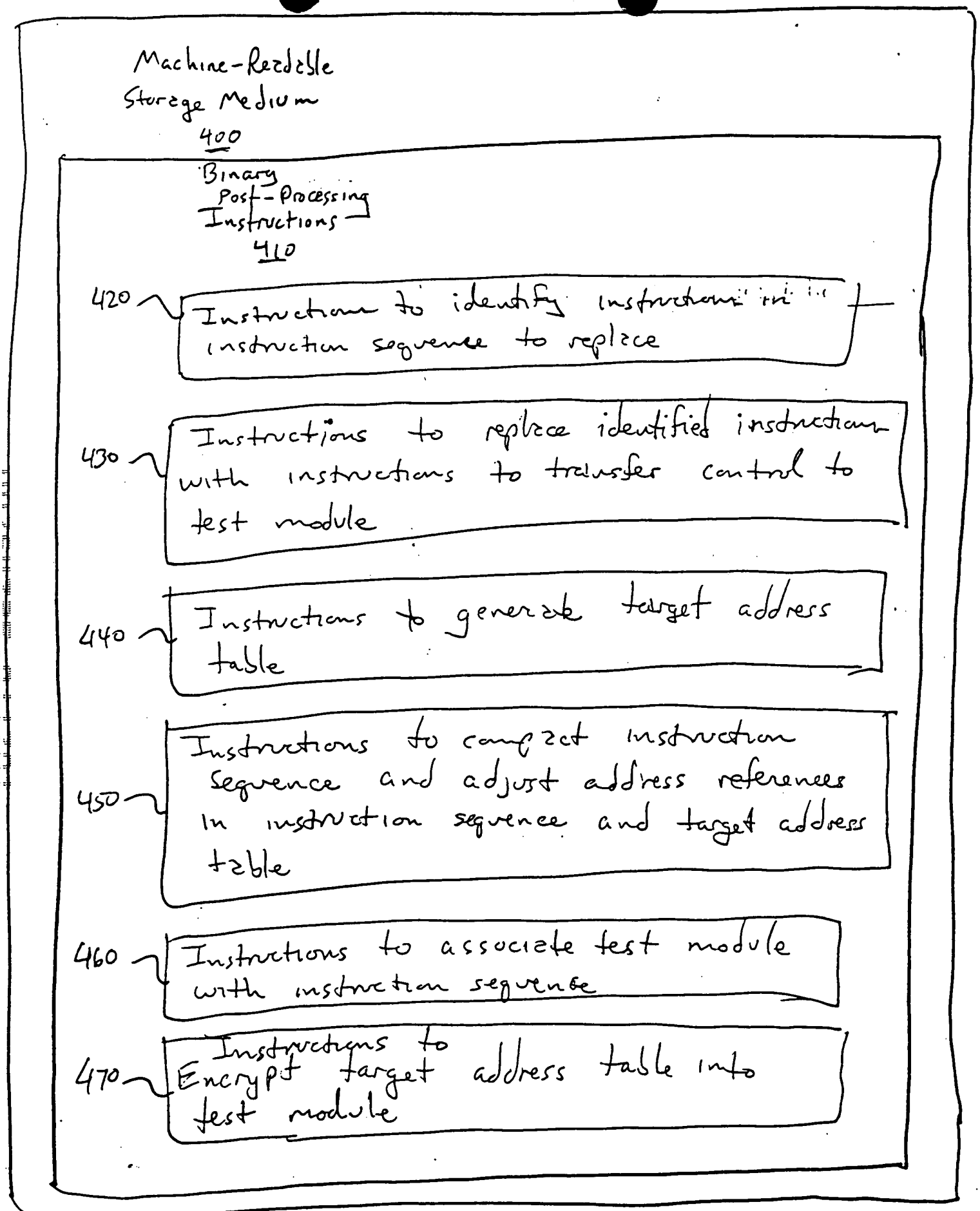
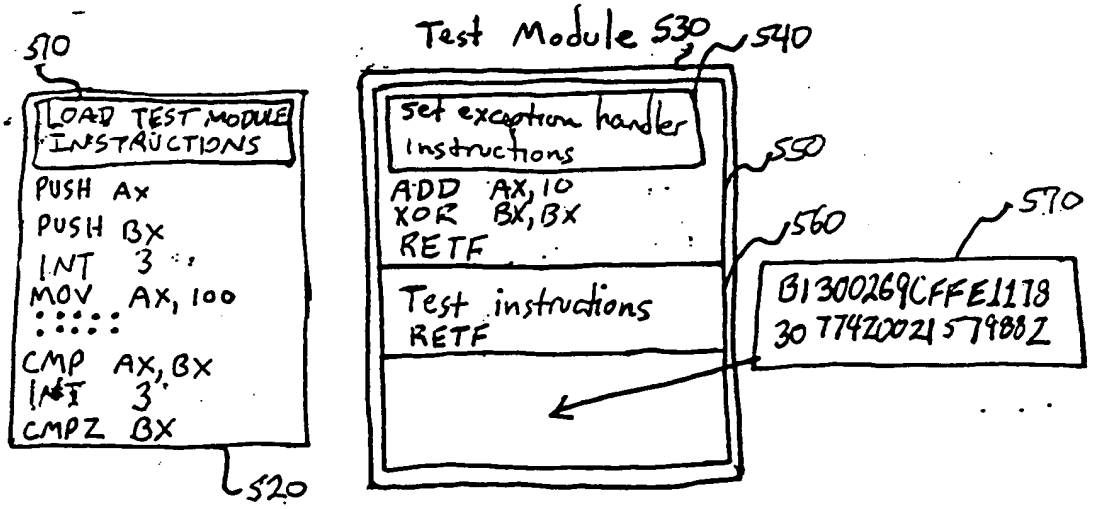

Machine-Readable Storage Medium
400
Binary Post-Processing Instructions
410
420
Instructions to identify instructions in instruction sequence to replace
430
Instructions to replace identified instructions with instructions to transfer control to test module
440
Instructions to generate target address table
450
Instructions to compact instruction sequence and adjust address references in instruction sequence and target address table
460
Instructions to associate test module with instruction sequence
470
Instructions to Encrypt target address table into test module

FIG. 5

Machine-Readable Storage Medium 500